10. Schematic Diagrams

10.1 Main Board(1/7)

Sheet	Description
1	INDEX
2	ASIC (CHORUS 2)
3	SDRAM, FLASH ROM
4	LSU, SMPS, PANEL, COVER OPEN
5	CIS, SCAN MOTOR
6	FUSER, MAIN MOTOR, CLUTCHES
7	PTL, FAN2
8	USB2.0, CRUM
9	
10	
11	
12	

Eiger
Main controller
REV 0.3

== MEMO ==

1. GPI PULL DOWN (GPI13,GPI15) -2PAGE
2. EEPROM DELETE -3PAGE
3. Add capacitor on SDRAM signal -3PAGE
4. CIS 12V PART JUMPER ADD - 5PAGE
5. CIS PART CAP CHANGE ON CLK AND SI LINE - 2.5PAGE
6. PTL CURRENT CONTROL RESISTOR CHANGE(510HM -> 620HM) - 6PAGE
7. MAIN MOTOR DRIVE IC PART ADD CAPACITOR ON VCC - 6PAGE
8. MAIN MOTOR CURRENT CONTROL RESISTOR CHANGE (1.4A) - 6PAGE

---------- CHANGES BETWEEN REV0.0 AND REV0.1----------

9. CRUM PORTS AND SCAN MOTOR PORTS ADDED -2PAGE
10. HOME DETECT PORT DELETED -2PAGE
11. X-TAL LOAD CAPS CHANGED TO 15pF -2PAGE
12. FLASH MEMORY EXTENSION CONSIDERATION (1MB->2MB) -3PAGE
13. ADDED R (1KOHM) ON PANEL RESET SIGNAL -4PAGE
14. SCAN MOTOR DRIVE IC CHANGED FROM L6219 TO A3977 -5PAGE
15. MAIN MOTOR IC CURRENT CHANGED -6PAGE
16. FUSER CIRCUIT CHANGED -6PAGE
17. CRUM CIRCUIT ADDED -7PAGE

Changed by: Ume	Date Changed: Monday, October 10, 2005	Time Changed: 2:31:48 pm	QA CHK	REV: 0.2	Drawing Number	Sheet 1 of 7

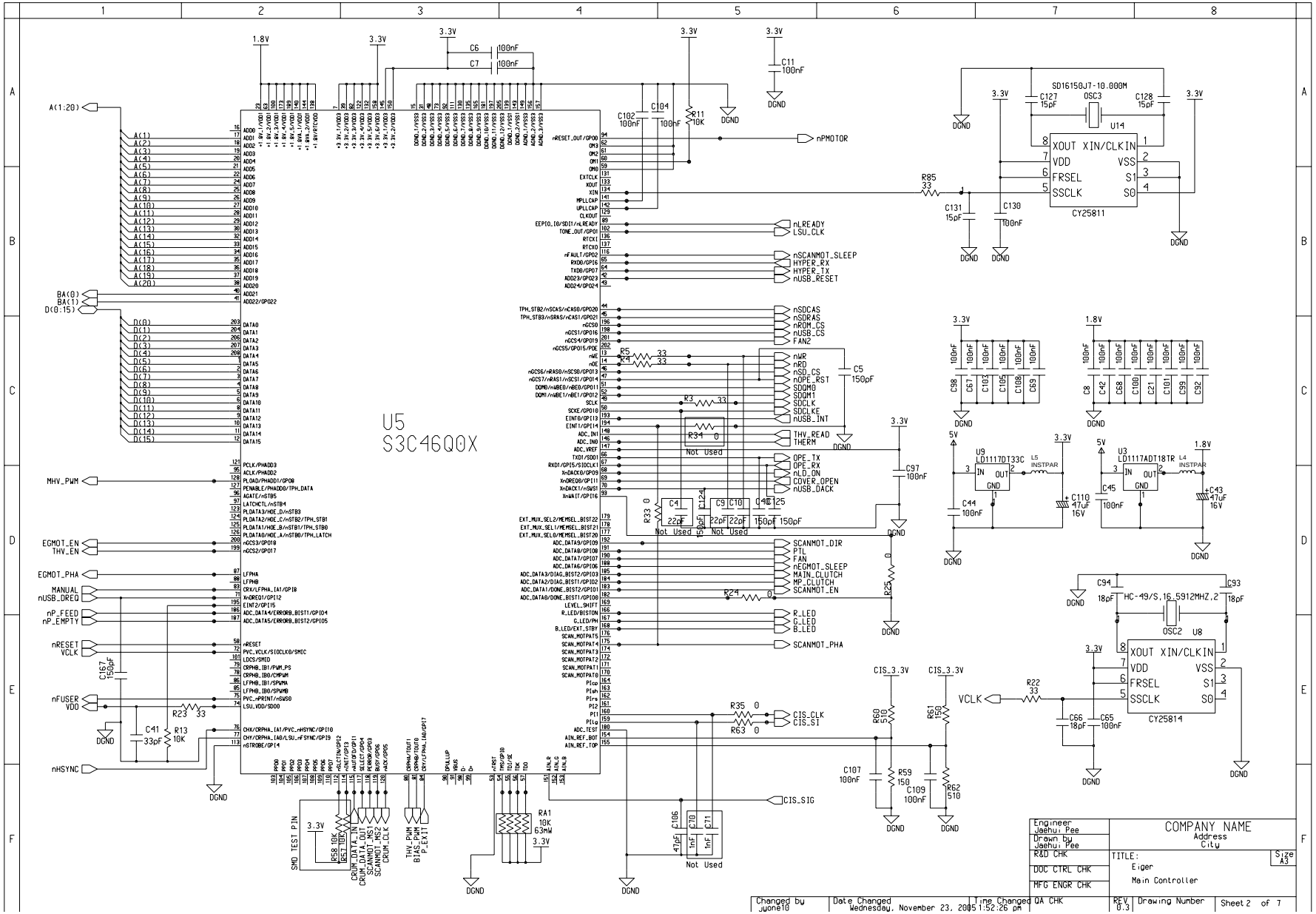
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Drawn by: Jaehu. Pee
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City

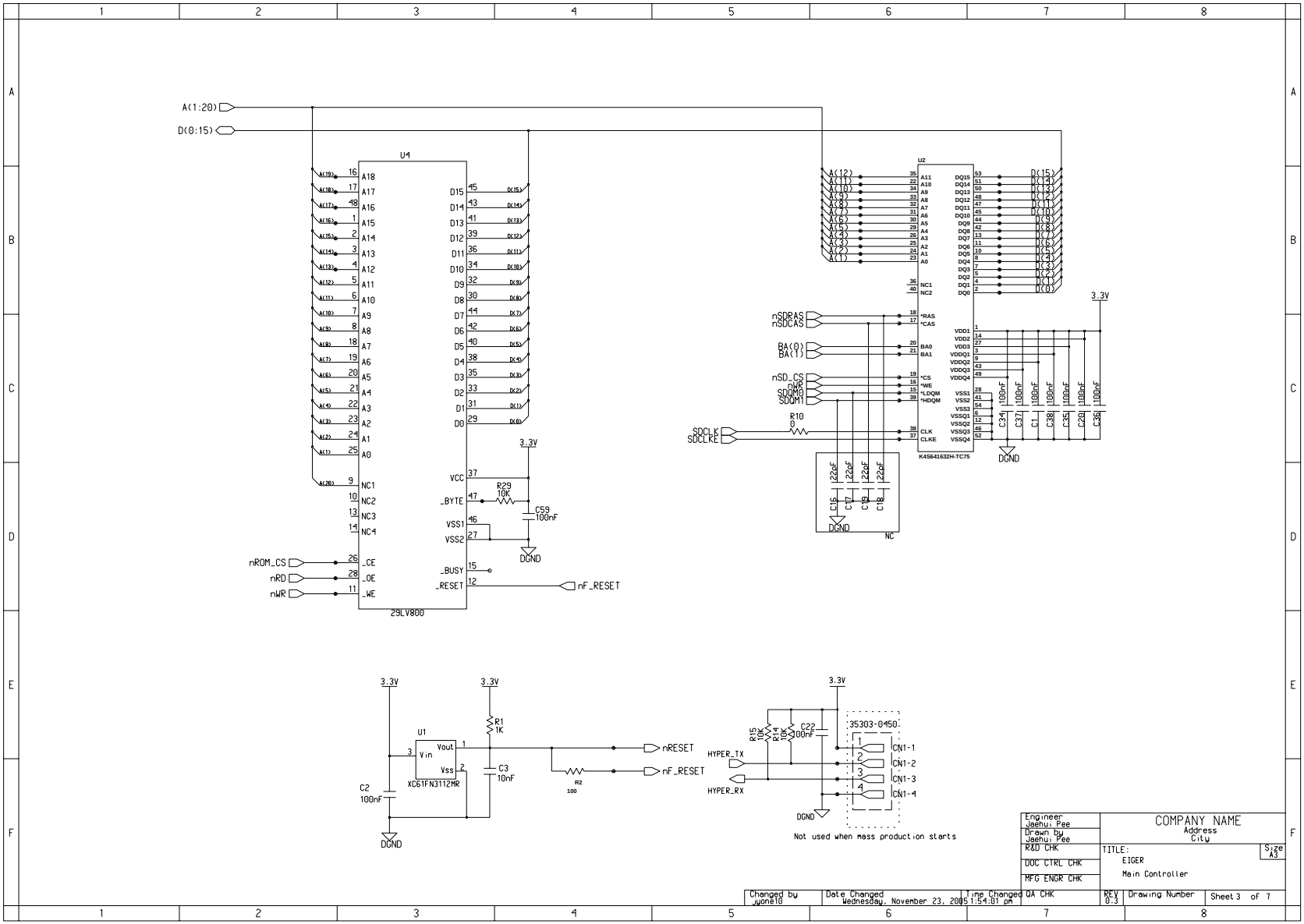
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Main Controller

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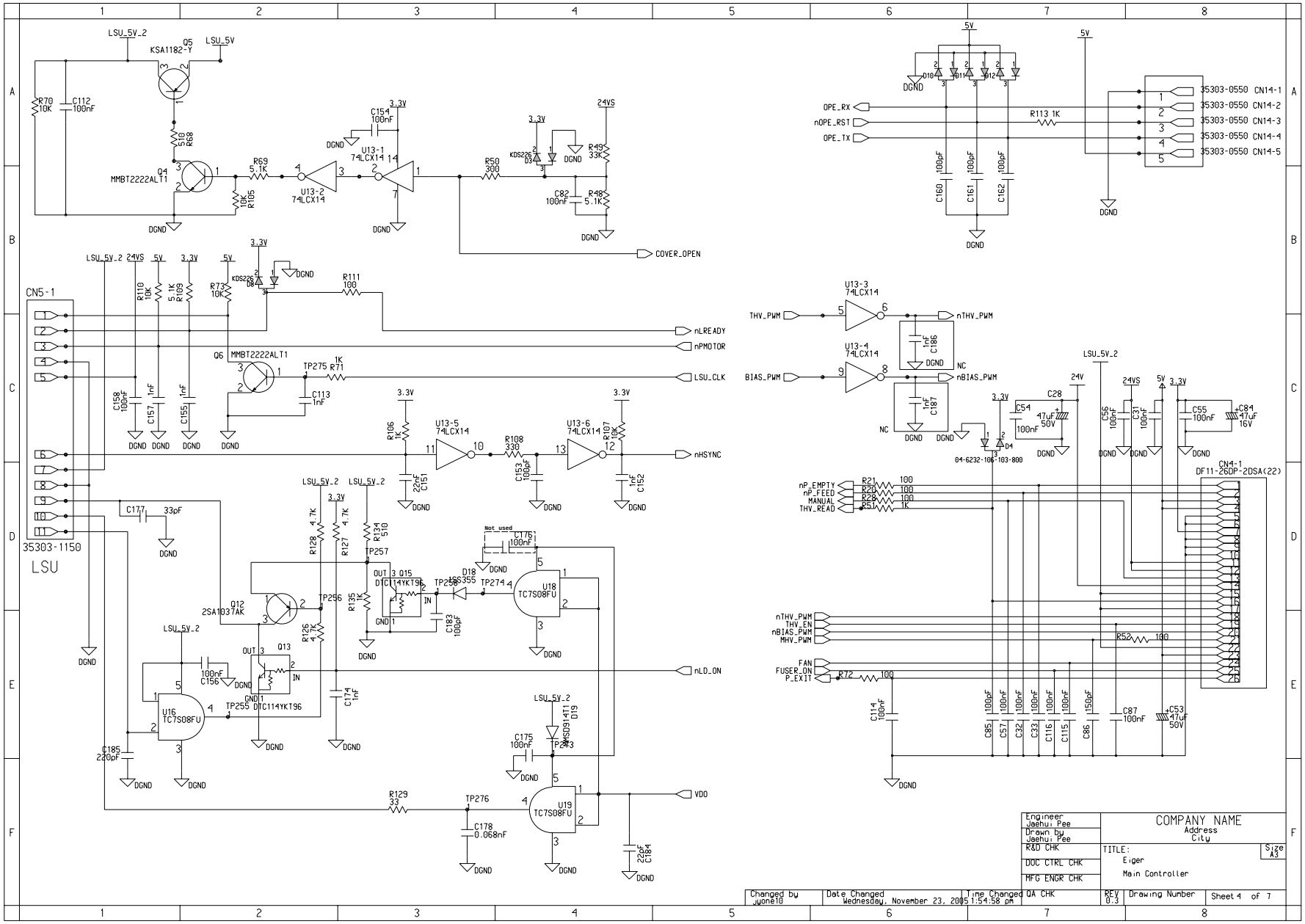
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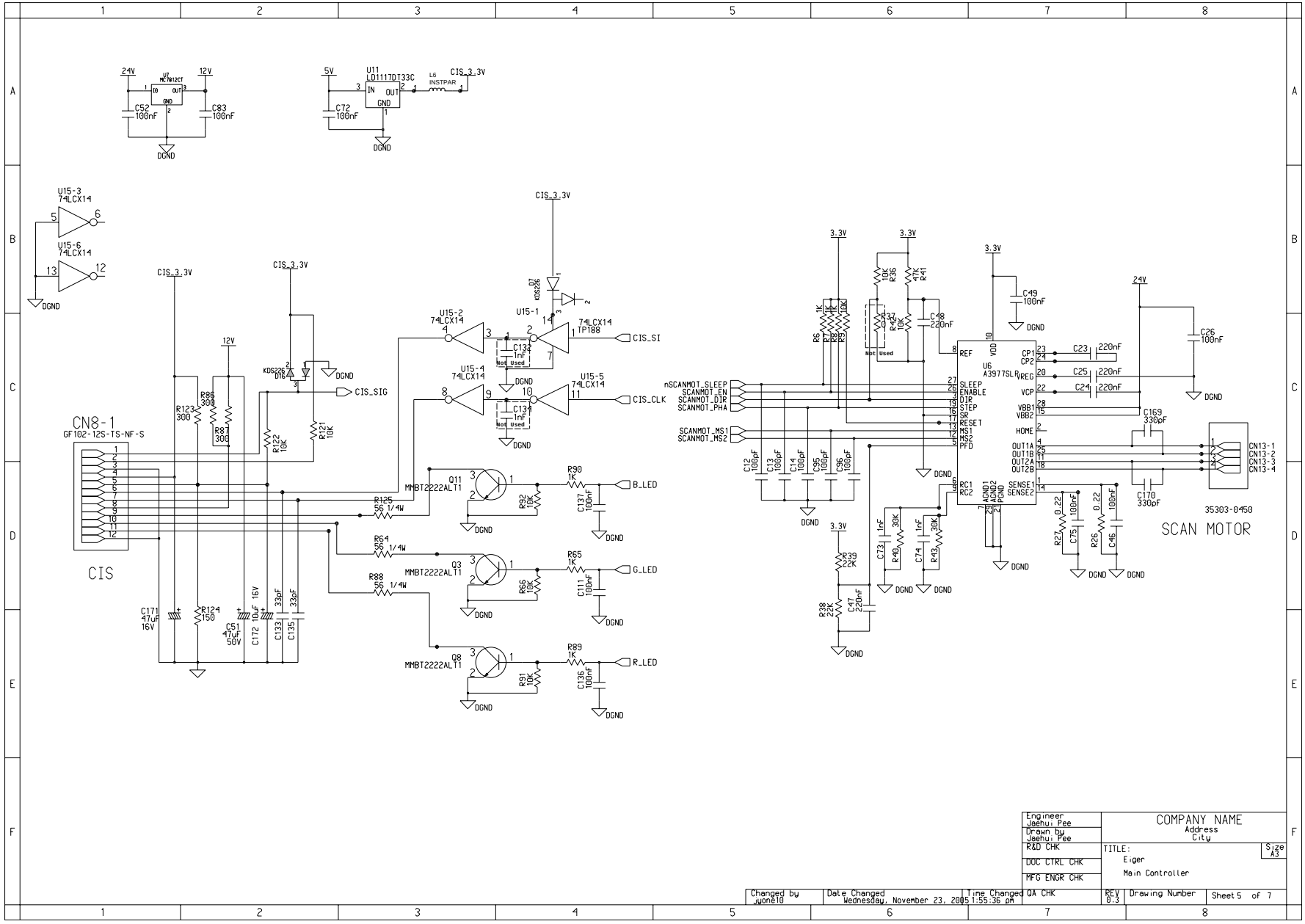
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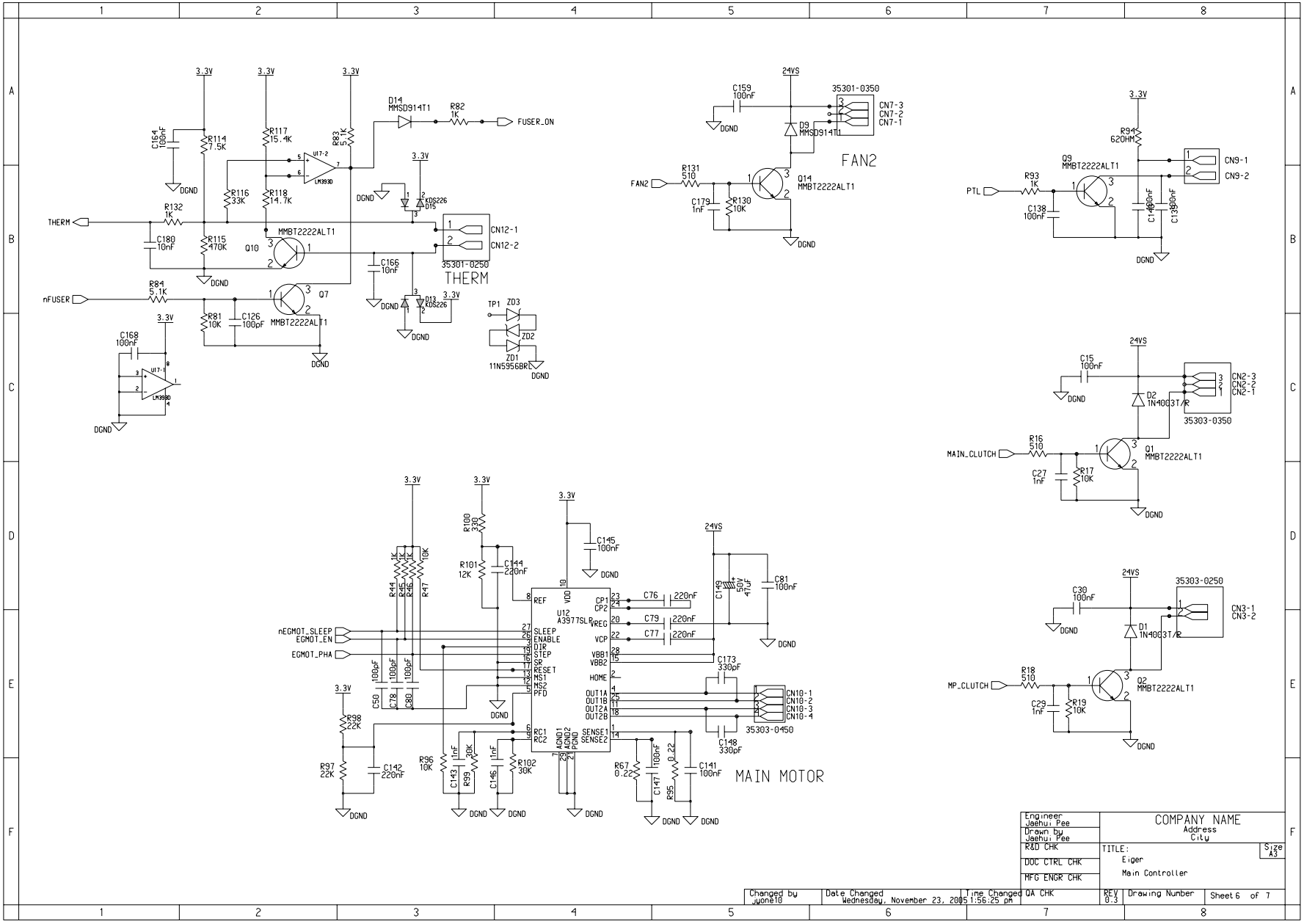
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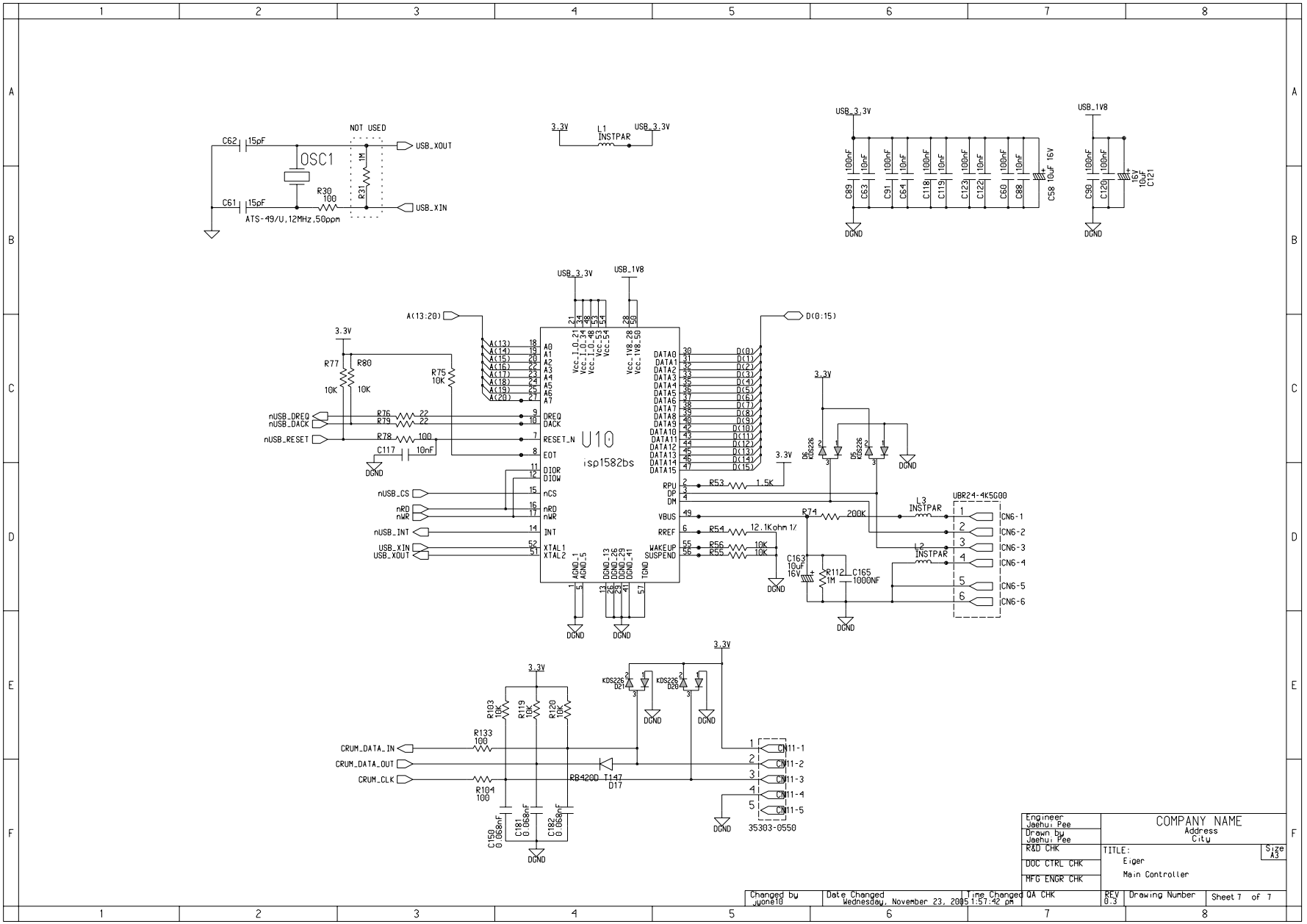
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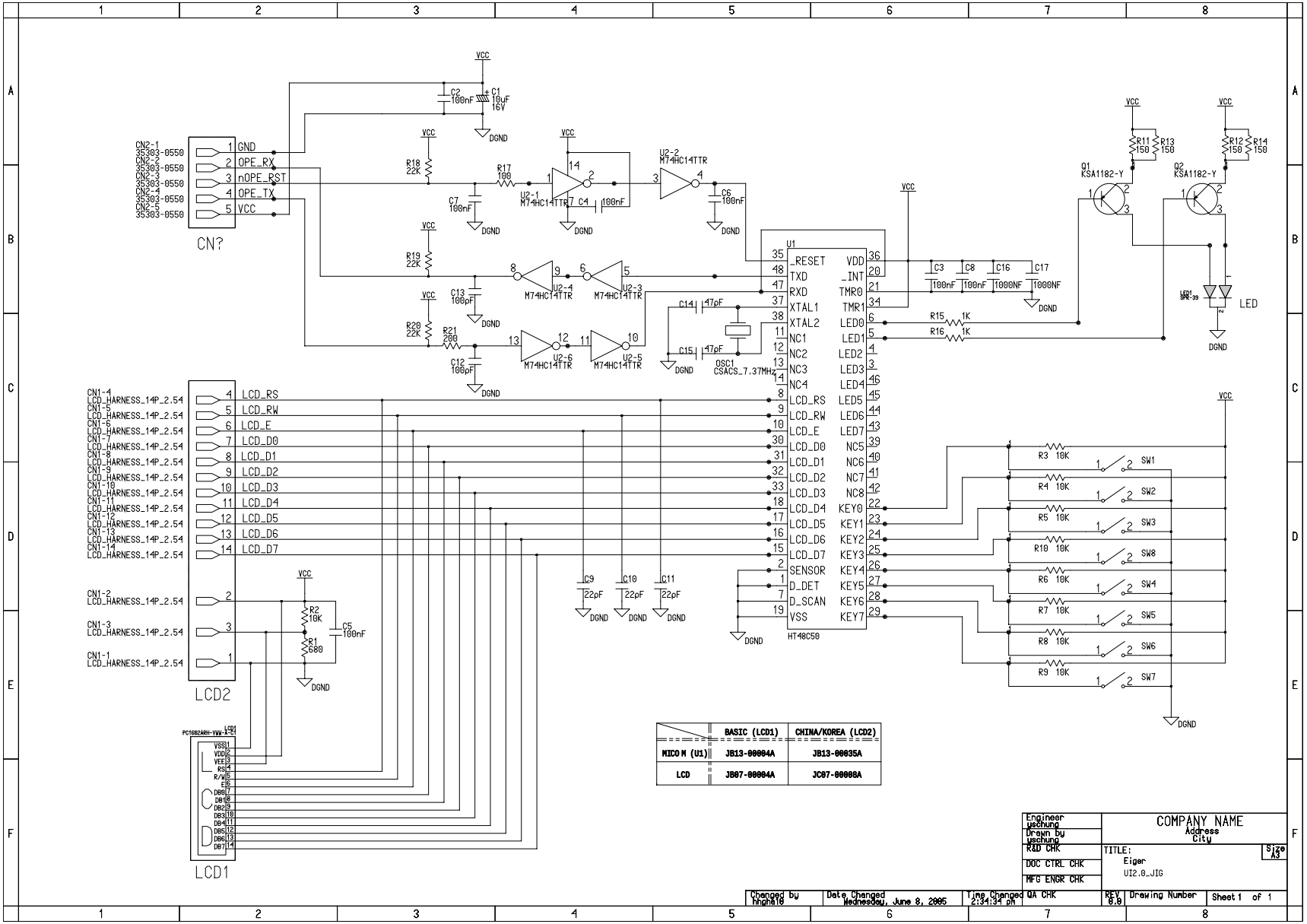
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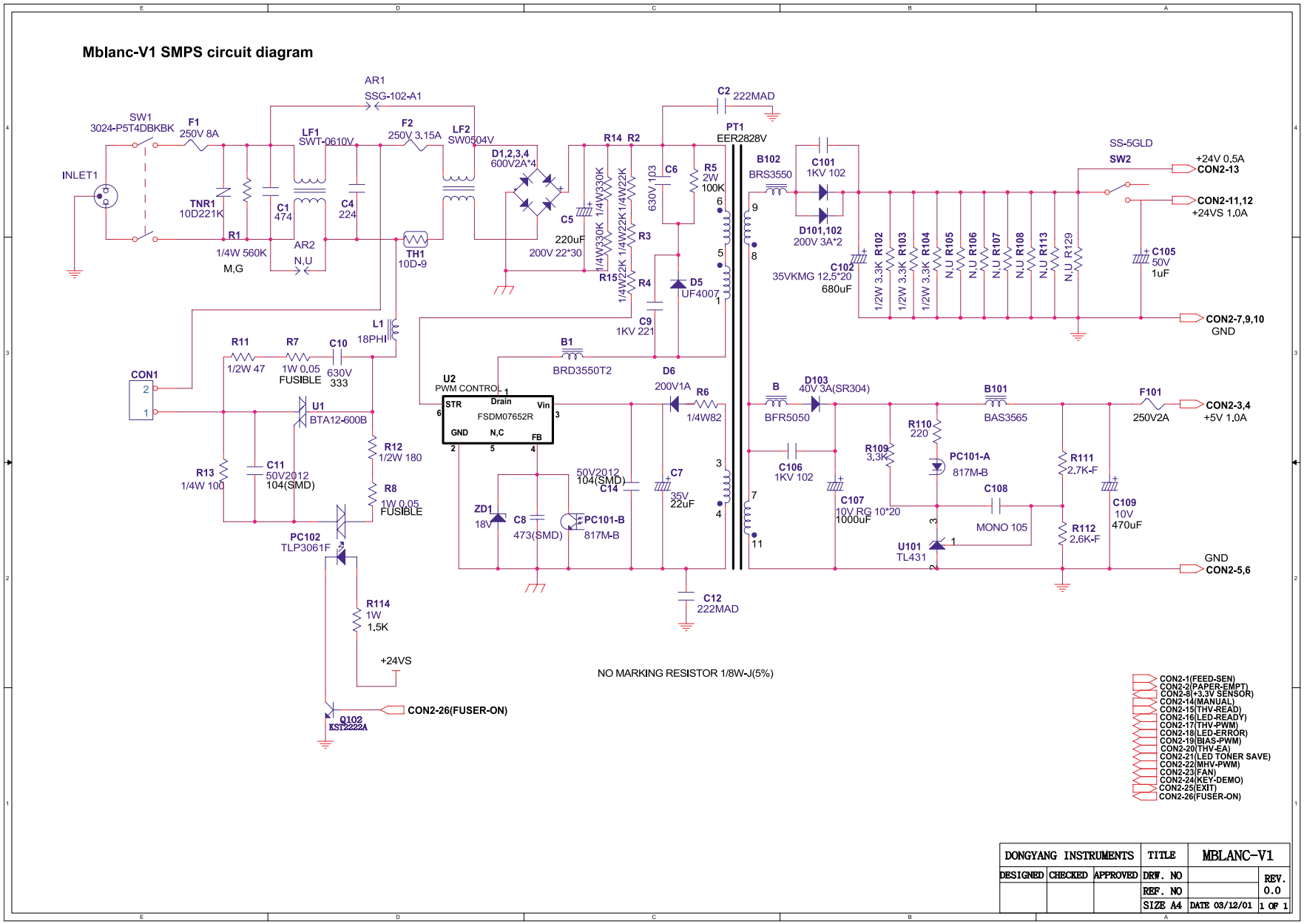
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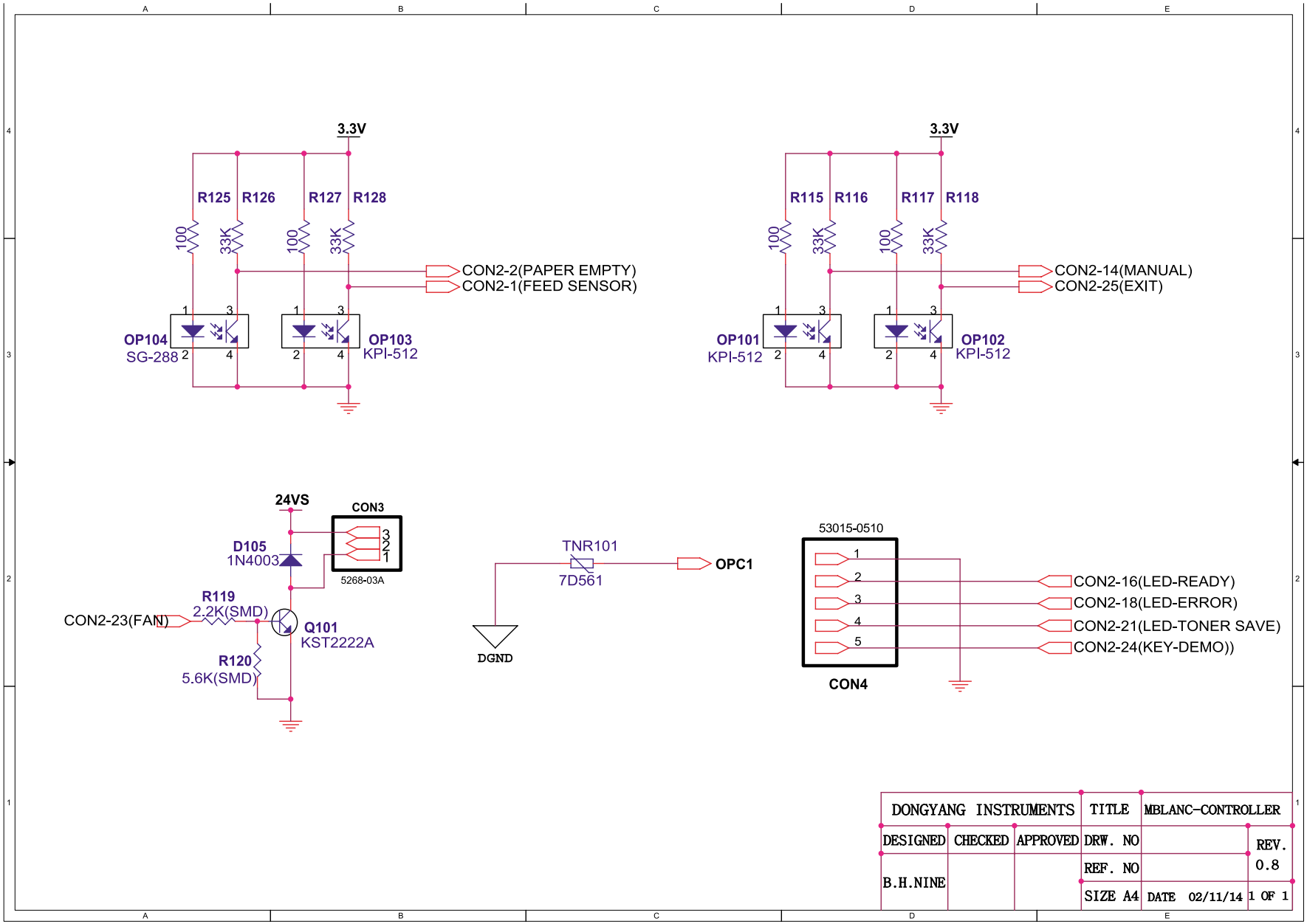
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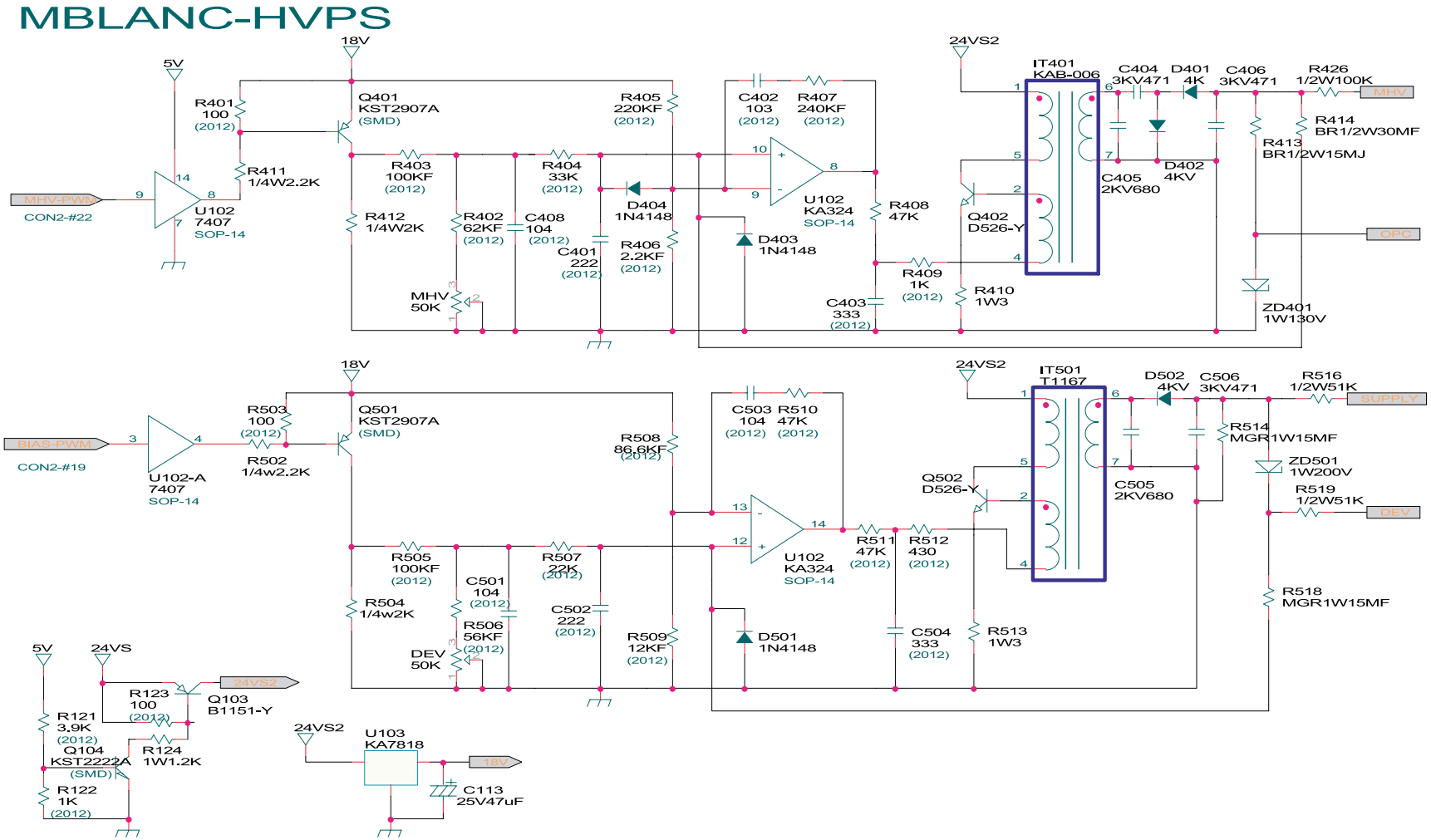
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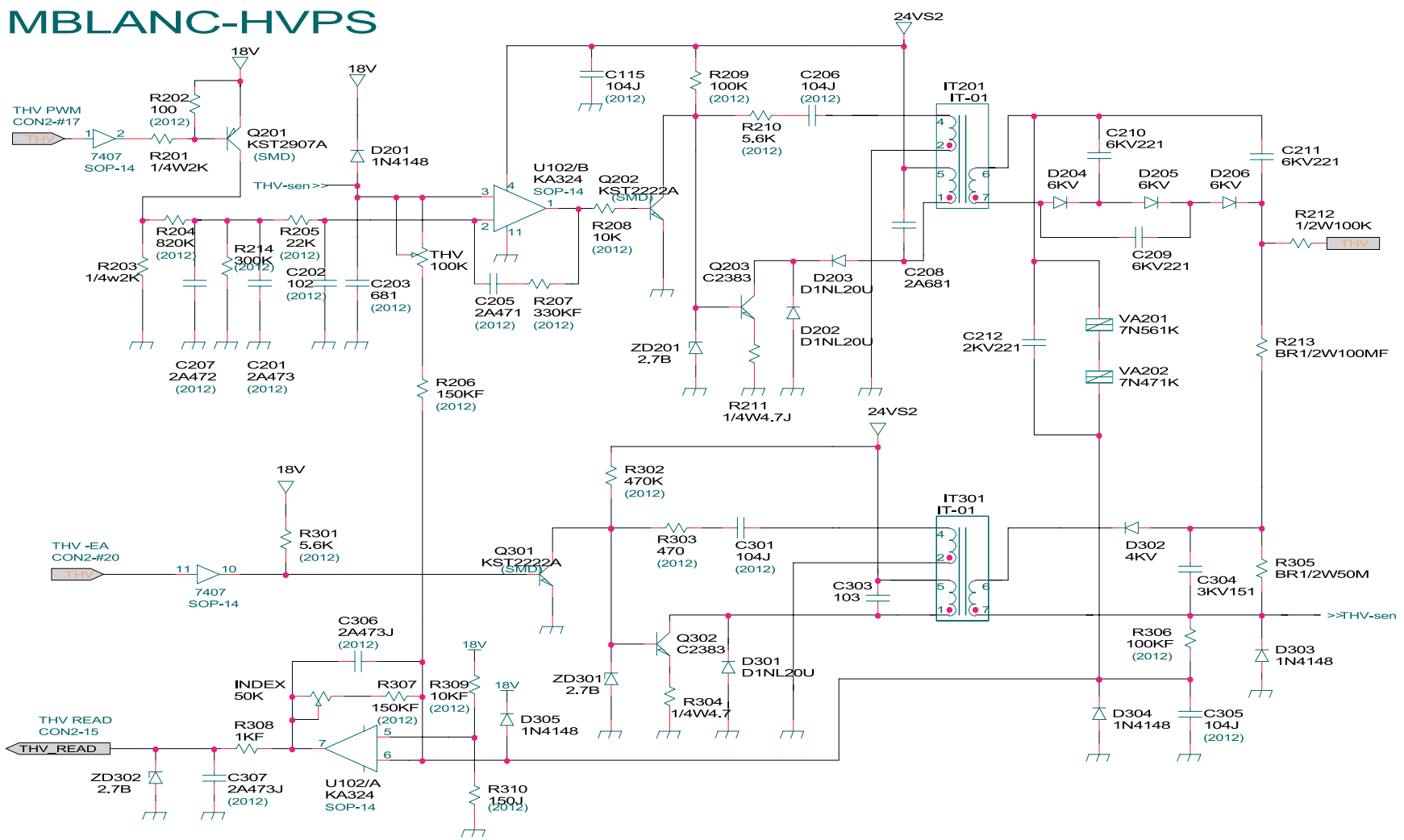
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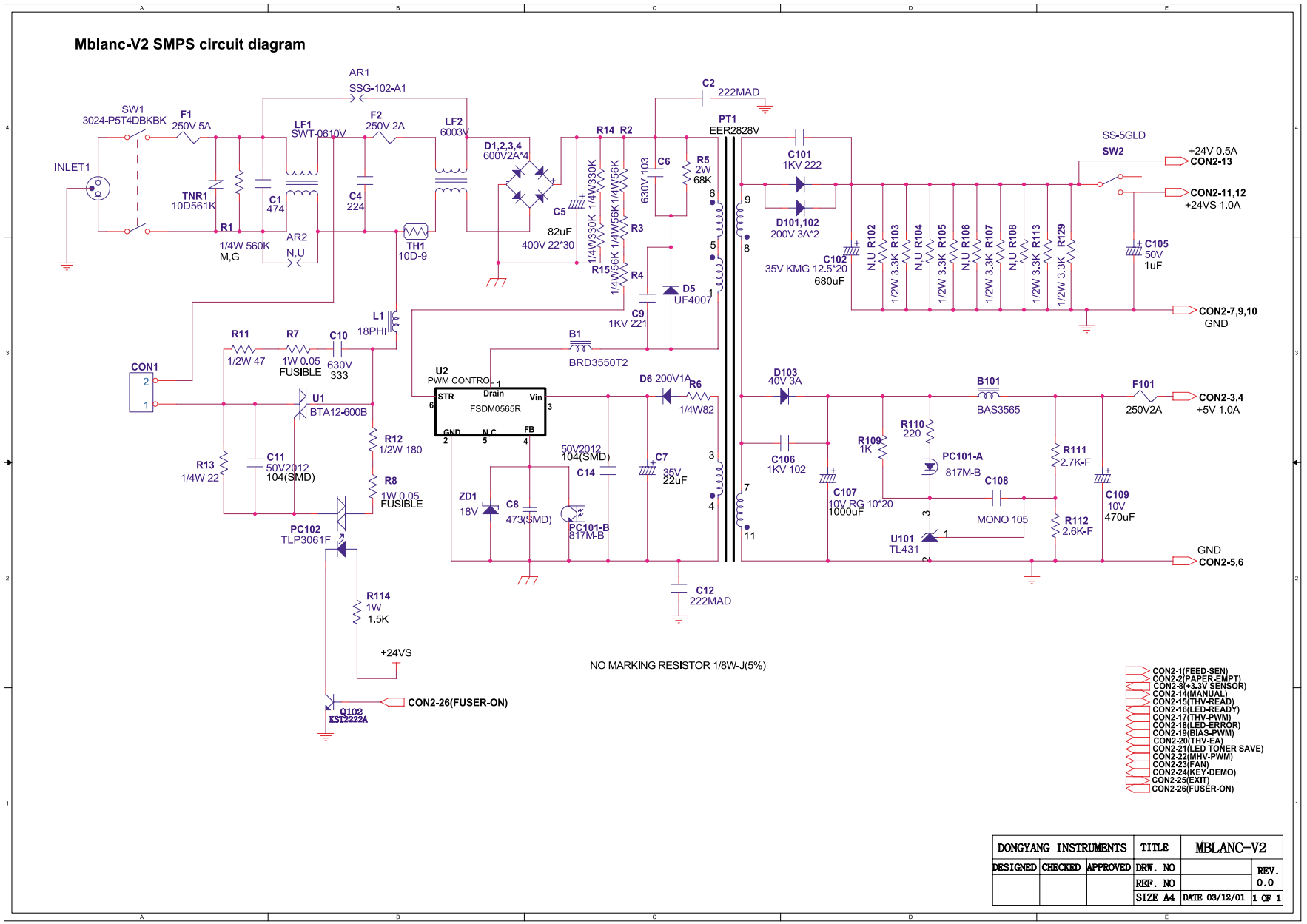
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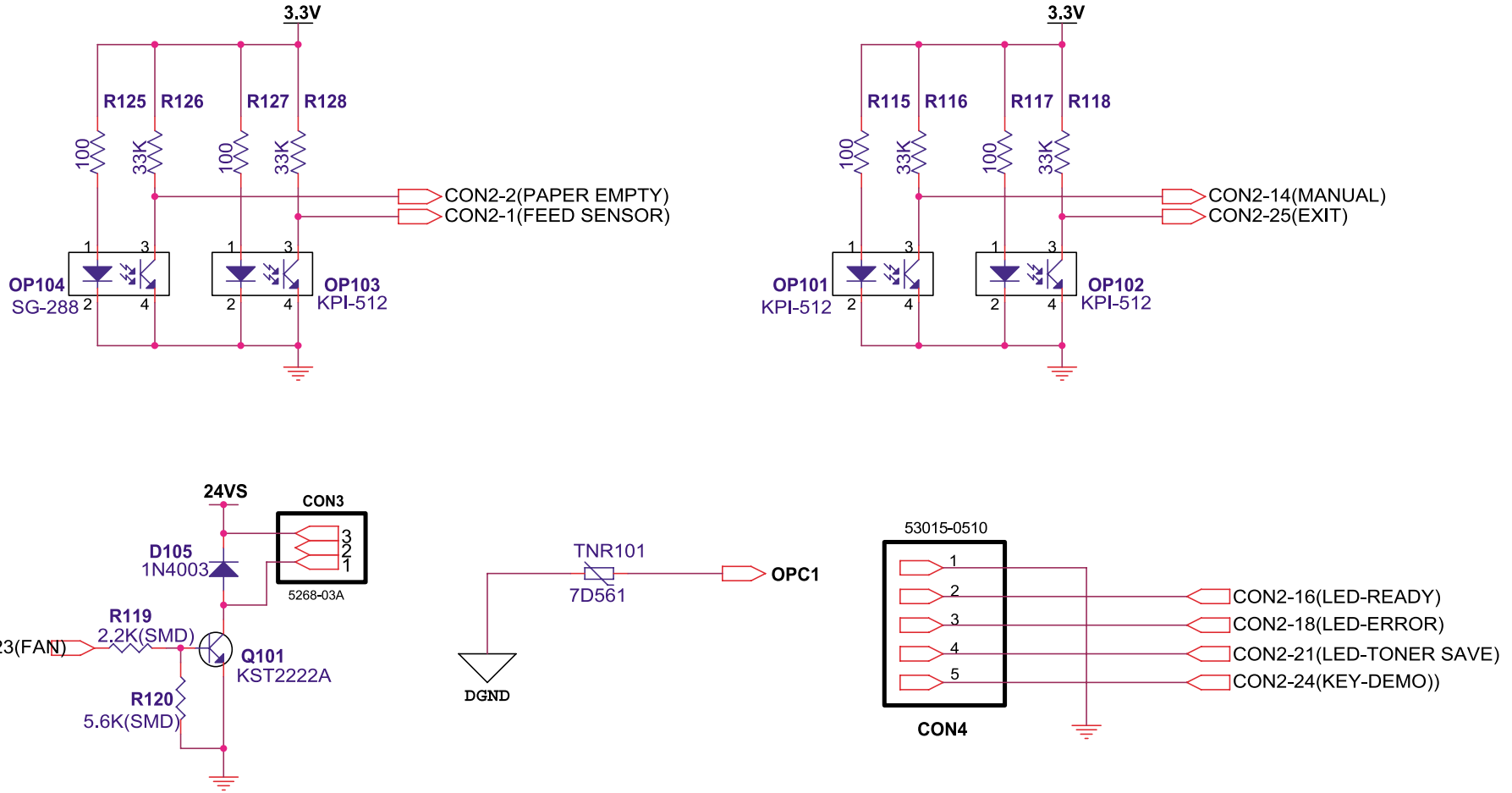
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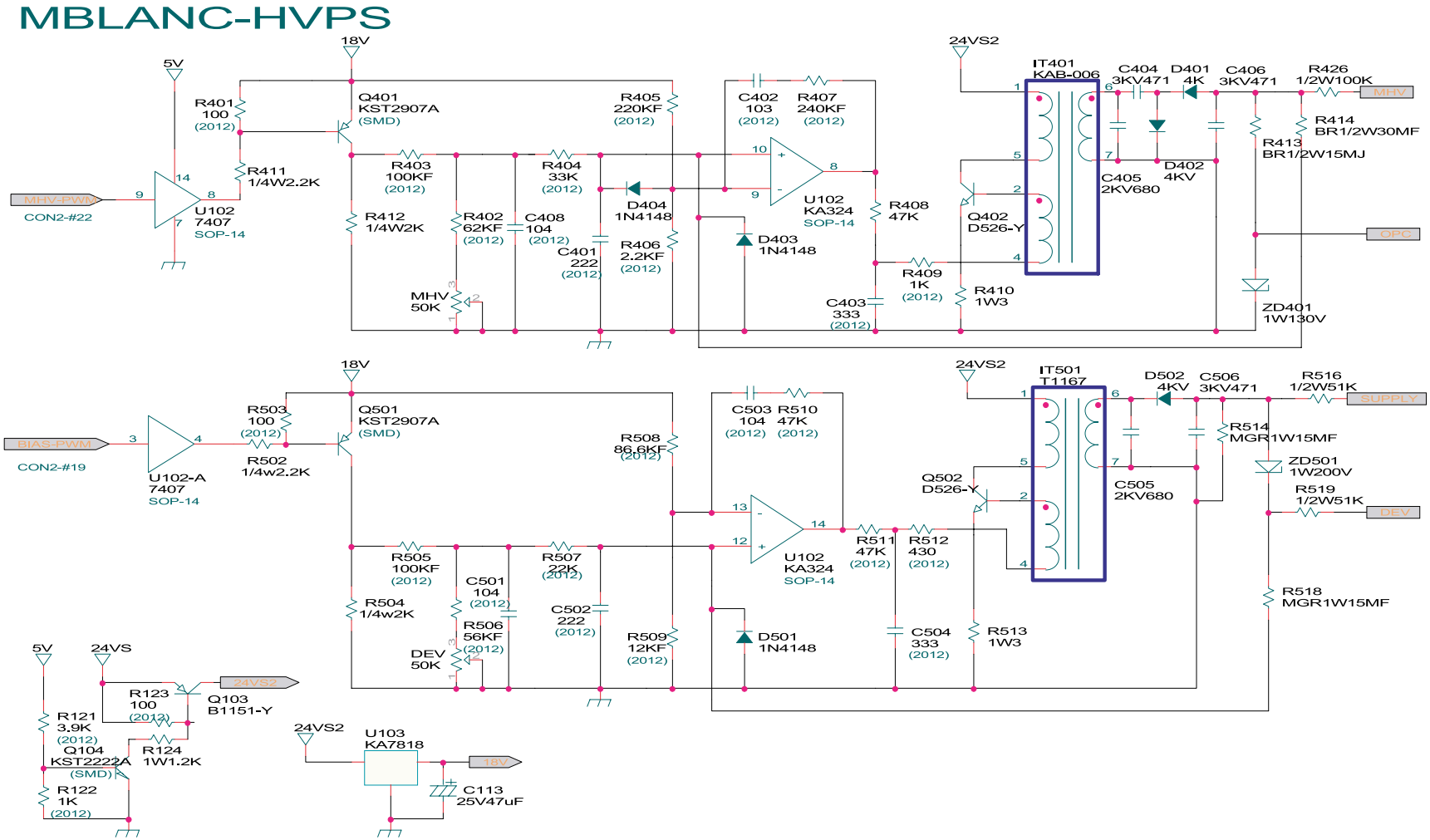


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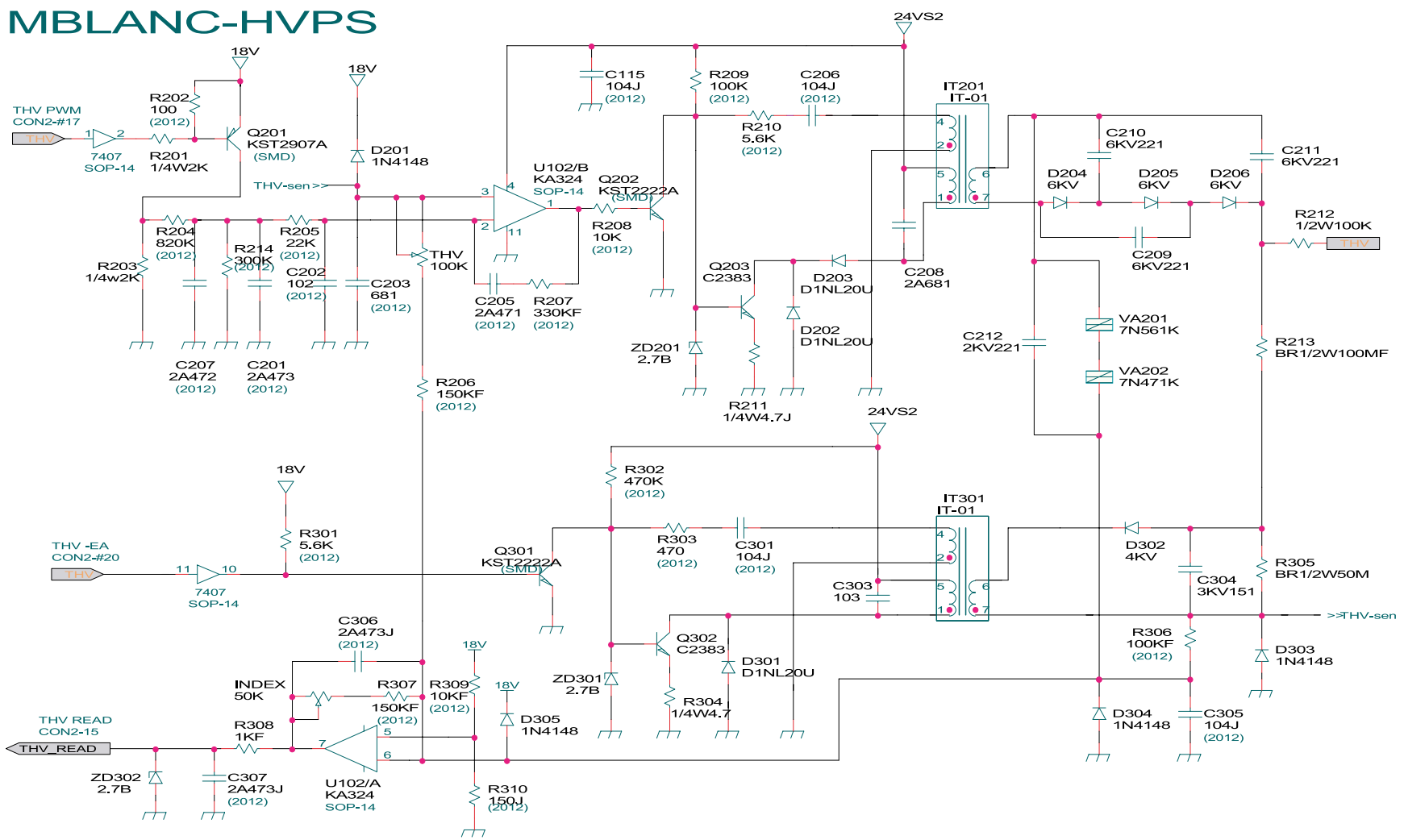


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